

<b>Substitute for Form 1449A/PTO (Modified)</b> (use as many sheets as necessary) Page 3 of 3				<b>Attorney Docket No.:</b> 5858P7100		<b>Application Number:</b> Not Yet Assigned	
				<b>First Named Inventor:</b> Krishna B. Rajan			
				<b>Filing Date:</b> Concurrently Herewith			

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U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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JLJ		LOGIC VISION, LOGIC BIST, 2 pages, <a href="http://www.logicvision.com/products/logic.html">http://www.logicvision.com/products/logic.html</a> .	
JLJ		LOGIC VISION, EMBEDDED LOGIC TEST, 1 page, <a href="http://www.logicvision.com/products/embeddedLogic.html">http://www.logicvision.com/products/embeddedLogic.html</a> .	
JLJ		CHAPTER 3., THE TEST ACCESS PORT (TAP), 9 pages, IEEE, Std. 1149.1-1990 (Includes IEEE Std 1149.1a-1993)	

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